

Abstracts

44 GHz Monolithic Low Noise Amplifier

J. Berenz, H.C. Yen, R. Esfandiari, K. Nakano, T. Sato, J. Velebir and K. Ip. "44 GHz Monolithic Low Noise Amplifier." 1987 Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 87.1 (1987 [MCS]): 15-18.

The design, fabrication, and performance of a single-stage 44 GHz monolithic HEMT low noise amplifier are described. The chip includes a single heterojunction HEMT with matching and biasing circuits. Greater than 5 dB gain was measured from 43.5 to 45.5 GHz and a noise figure of 5 dB with the associated gain of 5.5 dB was achieved at 44.5 GHz. The chip size is 1.25mm x 1.0mm.

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